AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions, and listings, of claims in the application.

Listing of Claims:

Claims 1-16 (Canceled).

Claim 17 (currently amended): A digital camera according to claim [[16]] 23, wherein said memory is an SDRAM, and said writer includes a buffer for holding the processed image data output from said processor, and a transferor for transferring to said memory the processed image data held by said buffer.

Claim 18 (currently amended): A digital camera according to claim [[16]] 23, wherein said memory has a plurality of memory areas, said digital camera further comprises a changer for changing a selecting of a memory area at an interval of the first time period, and wherein said writer writes the processed image data to one of said plurality of memory areas based on a changing result of said changer, and said reader reads the processed image data from another of said plurality of memory areas based on the changing result of said changer.

1   Claim 19 (previously presented): A digital camera according to claim 18, wherein said

2   changer changes the selecting of the memory area in a predetermined order.

1   Claim 20 (previously presented): A digital camera according to claim 18, wherein the

2   number of the memory areas is two, and the second time period is ½ of the first time period.

1   Claim 21 (currently amended): A digital camera according to claim [[16]] 23, wherein the

2   second time period is one over an integer of the first time period.

1   Claim 22 (currently amended): A digital camera according to claim [[16]] 23, wherein said

2   recorder records to said record medium the processed image data in a compressed state.

1   Claim 23 (currently amended): A digital camera according to claim 16, comprising:

2   an imaging device having an imaging surface which generates an image signal corresponding

3   to an optical image of an objective scene;

4   a processor for subjecting the image signal generated by said imaging surface to signal

5   processes including a thinning process so as to create processed image data at a rate of one screen

6   per a first time period;

7   a memory having a single input/output port;

8   a writer for writing to said memory the processed image data output from said processor;

9   a reader for reading the processed image data stored in said memory at a rate of one screen

10   per a second time period which is shorter than the first time period;

11   a displayer for displaying an image based on the processed image data read out by said

12   reader;

13   a first instructor for instructing said processor to suspend the thinning process at a time of

14   accepting a recording operation; and

15   a recorder for recording to a record medium the processed image data stored in said memory

16   in response to the recording operation, and further comprising a second instructor for instructing said

17   reader to suspend a reading process in association with an instructing process of said first instructor.

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